```
ENTITY mx2_x2 IS
   CONSTANT cin_i0 : NATURAL := 2250;

CONSTANT cin_i0 : NATURAL := 17;

CONSTANT cin_i0 : NATURAL := 17;
GENERIC (
   CONSTANT cin_i0
CONSTANT cin_i1
                                                 : NATURAL := 9;
    CONSTANT rdown_cmd_q : NATURAL := 1620;
   CONSTANT rdown_cmd_q : NATURAL := 1620;
CONSTANT rdown_i0_q : NATURAL := 1620;
CONSTANT rdown_i1_q : NATURAL := 1620;
CONSTANT rup_cmd_q : NATURAL := 1790;
CONSTANT rup_i0_q : NATURAL := 1790;
CONSTANT rup_i1_q : NATURAL := 1790;
CONSTANT tphh_i0_q : NATURAL := 451;
CONSTANT tphh_i1_q : NATURAL := 451;
CONSTANT tplh_i0_q : NATURAL := 469;
CONSTANT tplh_cmd_q : NATURAL := 469;
CONSTANT tphh_cmd_q : NATURAL := 484.
  CONSTANT tphh_cmd_q : NATURAL := 484;
CONSTANT tphl_cmd_q : NATURAL := 485;
CONSTANT tpll_cmd_q : NATURAL := 522;
CONSTANT tplh_cmd_q : NATURAL := 534;
CONSTANT transistors : NATURAL := 12
);
PORT (
   cmd
                 : IN BIT;
                : IN BIT;
   iΟ
                : IN BIT;
   i1
                : OUT BIT;
   q
   vdd : IN BIT;
  vss : IN BIT
) ;
END mx2_x2;
ARCHITECTURE vbe OF mx2_x2 IS
BEGIN
   ASSERT ((vdd and not (vss)) = '1')
   REPORT "power supply is missing on mx2_x2"
   SEVERITY WARNING;
  q <= ((i1 AND cmd) OR (NOT (cmd) AND i0)) AFTER 1100 ps;
END;
```