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The concept

Generic Architecture means we will define some SoC hardware architecture with few variable parameters. The main goal is to change number of Cpus or memory banks, but not main architecture like interconnect choice.

Implementation

An architecture is defined in a python function returning a `soclib.Architecture`.

Sample code

There are many examples in DSX examples. `source:trunk/dsx/examples/mjpeg/vgmn_noirq_mono.py` is a good starting point.