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#!/usr/bin/env python

import soclib

def _cluster(arch, no,
             with_gdb,
             ncpu, nram,
             icache_lines, icache_words,
             dcache_lines, dcache_words):
    crossbar = arch.create('caba:vci_local_crossbar', 'lc%d'%no)

    for i in range(ncpu):
        cn = arch.cpu_num
        arch.cpu_num += 1
        if with_gdb:
            mips = arch.create('caba:iss_wrapper', 'mips%d_%d'%(no,i), iss_t='common')
        else:
            mips = arch.create('caba:iss_wrapper', 'mips%d_%d'%(no,i), iss_t='common')
        xcach = arch.create('caba:vci_xcach', 'xcach%d_%d'%(no,i),
                           icache_lines = icache_lines,
                           icache_words = icache_words,
                           dcache_lines = dcache_lines,
                           dcache_words = dcache_words)

        mips.dcach // xcach.dcach
        mips.icach // xcach.icach

        crossbar.to_initiator.new() // xcach.vci

    for i in range(2):
        ram = arch.create('caba:vci_ram', 'ram%d_%d'%(no,i))
        base = 0x10000000*(1+no)+0x1000000*i
        ram.addSegment('cram%d_%d'%(no,i), base, 0x100000, True)
        ram.addSegment('uram%d_%d'%(no,i), base + 0x200000, 0x100000, False)
        ram.vci // crossbar.to_target.new()
        if i == 0 and no == 0:
            ram.addSegment('boot', 0xbfc00000, 0x800, True)
            ram.addSegment('excep', 0x80000080, 0x800, True)
    return crossbar

def ClusteredNoirqMulti(
    nbcpu = [1],
    nbram = [1],
    with_gdb = False,
    icache_lines = 32,
    icache_words = 8,
    dcache_lines = 32,
    dcache_words = 8 ):
    arch = soclib.Architecture(
        cell_size = 4,
        plen_size = 1,
        addr_size = 32,
        rerror_size = 1,
        clen_size = 1,
        rflag_size = 1,
        srcid_size = 8,
        pktid_size = 1,
        trdid_size = 1,
        wrplen_size = 1
    )

    arch.cpu_num = 0

    arch.create('common:mapping_table',
               'mapping_table',
               addr_bits = [5,3],
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        srcid_bits = [4,4],
        cacheability_mask = 0xc00000)
arch.create('common:elf_loader', 'loader')

vgmn = arch.create('caba:vci_vgmn', 'vgmn0', min_latency = 2, fifo_depth = 8)

for no, ncpu, nram in zip(range(len(nbcpu)), nbcpu, nbram):
    lc = _cluster(
        arch, no,
        with_gdb = with_gdb,
        ncpu = ncpu, nram = nram,
        icache_lines = icache_lines, icache_words = icache_words,
        dcache_lines = dcache_lines, dcache_words = dcache_words)
    vgmnToInitiator = lc.initiator_to_up
    vgmnToTarget = lc.target_to_up

tty = arch.create('caba:vci_multi_tty', 'tty', names = ['tty0'])
tty.addSegment('tty', 0x95000000, 0x20, False)
tty.vci // lc.to_target.new()

return arch

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