

SoCLib Architecture support

SoCLib is an open platform for virtual prototyping of multi-processors system on chip (MP-SoC). From a software engineer point of view, SoCLib can be seen as an library of hardware component models which can be used to build various multi-processor machine emulators. MutekH can run on SoCLib based simulator.

MutekH supports the SoCLib architecture specificities:

- drivers for SoCLib hardware components
- support for instrumentation-specific CPU registers
- heterogeneous platforms (platforms with ONE MutekH instance, but with different CPU architectures running at the same time, sharing OS global data)
- integrated build system and workflow

You may be interested in reading the [Tutorial for SoCLib users](#)

You may want to visit the [?SoCLib](#) project home page to learn more.

SoCLib platform specificities

- [Xicu in Flattened device trees](#)