

Construction d'un OS pour système embarqué

Périphériques		Segments dans la ROM		Segments dans la RAM	
TIMER_BASE	0xd3200000	KTEXT_LMA_BASE	0xbf800000	RAM_BASE	0x7F400000
TIMER_SIZE	0x00000080	KTEXT_LMA_SIZE	0x00020000	RAM_SIZE	0x01000000
ICU_BASE	0xd2200000	KDATA_LMA_BASE	0xbf820000	KTEXT_BASE	0x80000000
ICU_SIZE	0x00000020	KDATA_LMA_SIZE	0x00020000	KDATA_BASE	0x80020000
				KDATA_SIZE	0x003E0000
DMA_BASE	0xd1200000	UTEXT_LMA_BASE	0xbf840000	USR_TEXT_BASE	0x7F400000
DMA_SIZE	0x00000014	UTEXT_LMA_SIZE	0x00060000	USR_DATA_BASE	0x7F460000
TTY_BASE	0xd0200000	UDATA_LMA_BASE	0xbf8A0000	USR_DATA_SIZE	0x00B9F000
TTY_SIZE	0x00000040	UDATA_LMA_SIZE	0x00020000		
BD_BASE	0xd5200000	BOOT_BASE	0xbfc00000		
BD_SIZE	0x20	BOOT_SIZE	0x00001000		

TME

- Démarrage...