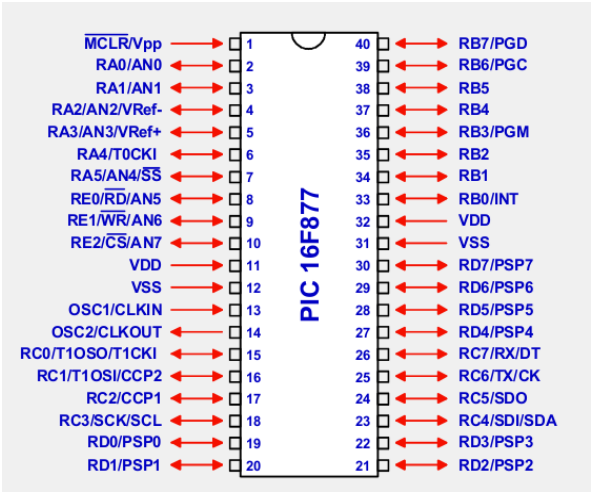
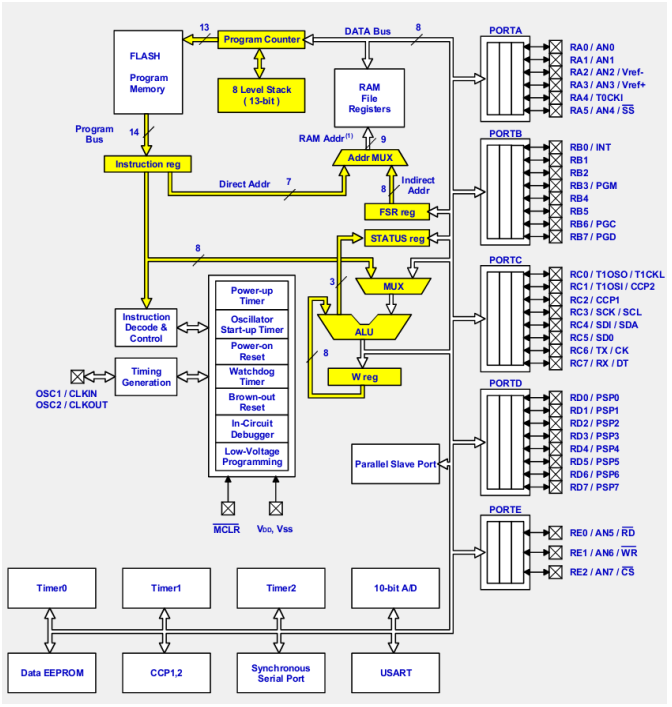
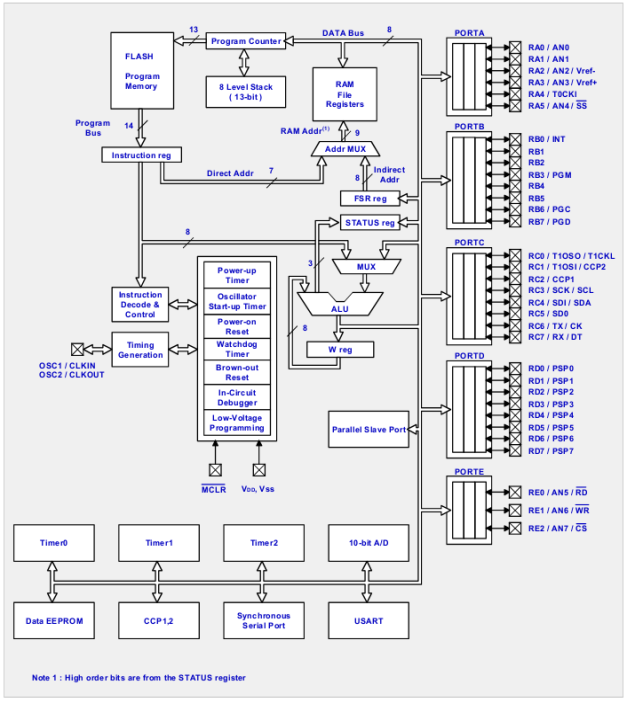
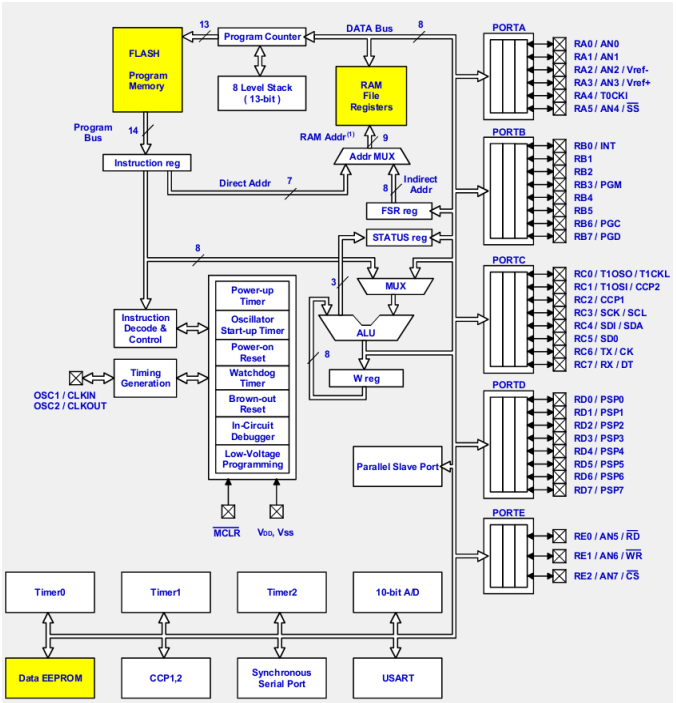




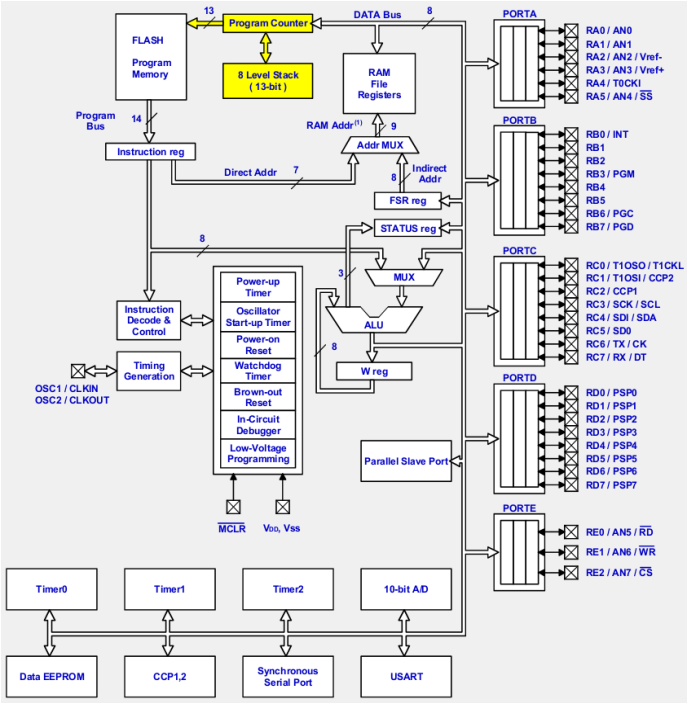
Le CPU du PIC 16F877



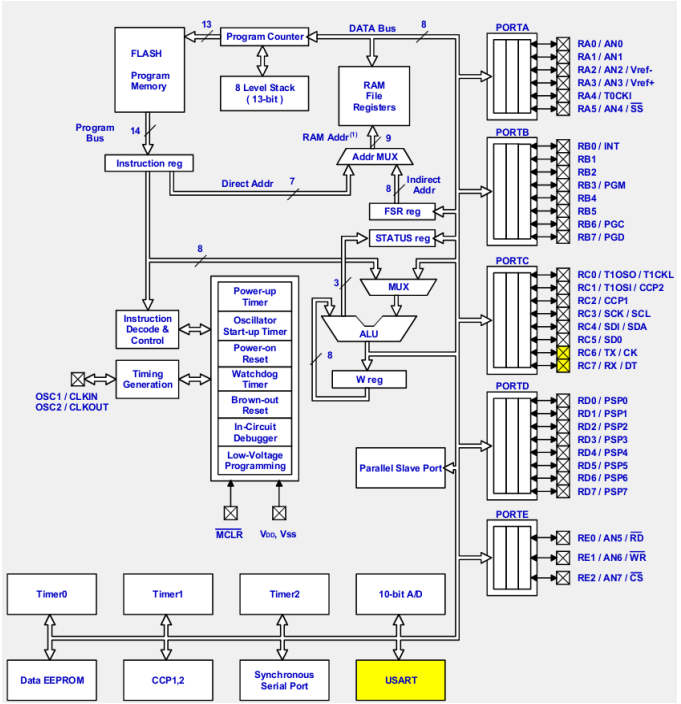
Structure interne du PIC 16F877



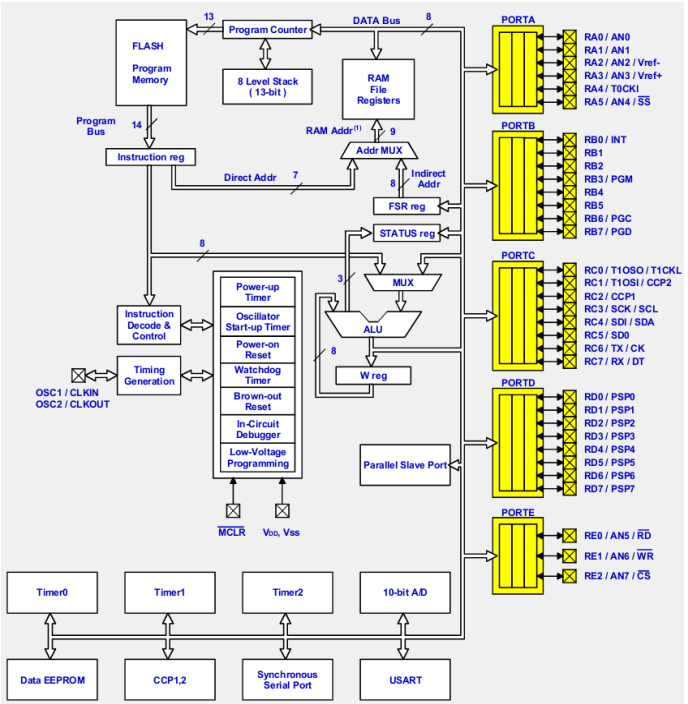
Les mémoires du PIC 16F877



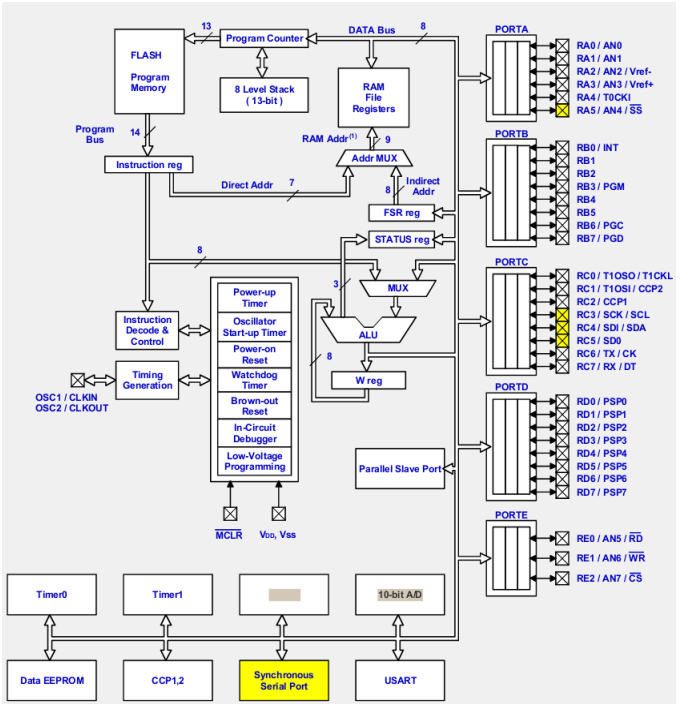
Le système de pile du PIC 16F877

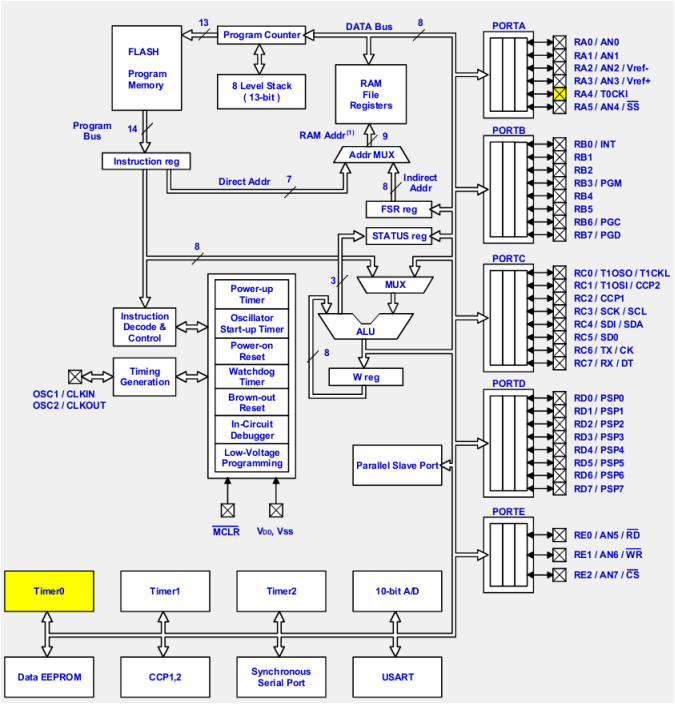


L'USART (Universal Asynchronous Synchronous Receiver Transmitter) du PIC 16F877

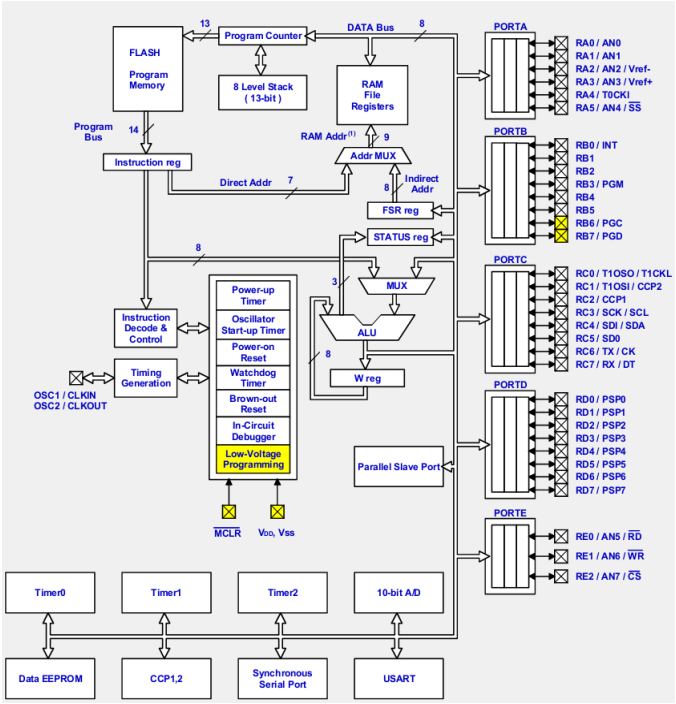


Les ports d'E/S du PIC 16F877

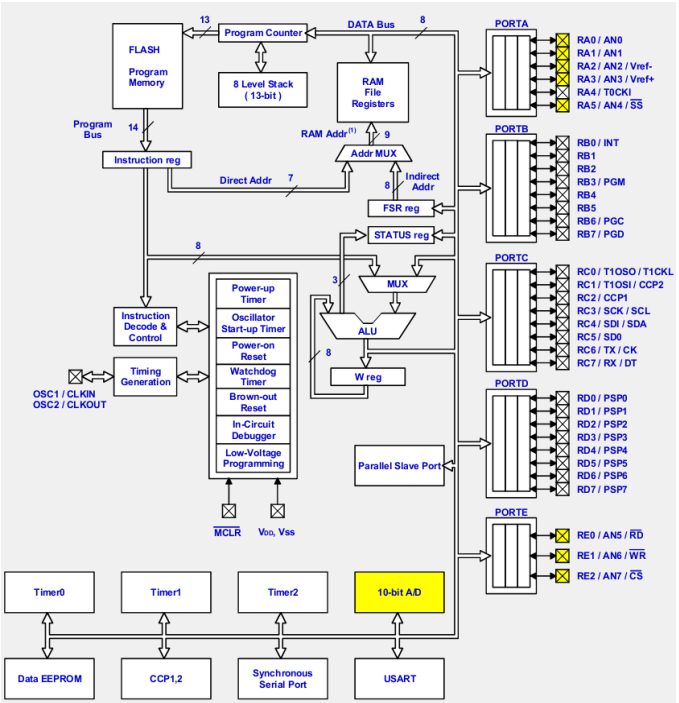




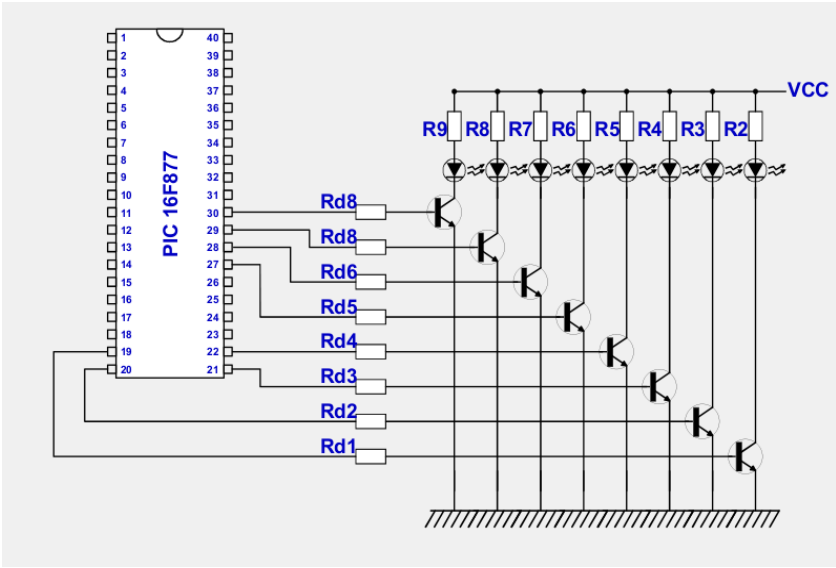
Timer0 du PIC 16F877



Le système ICSP (In-Circuit Serial Programming) du PIC 16F877

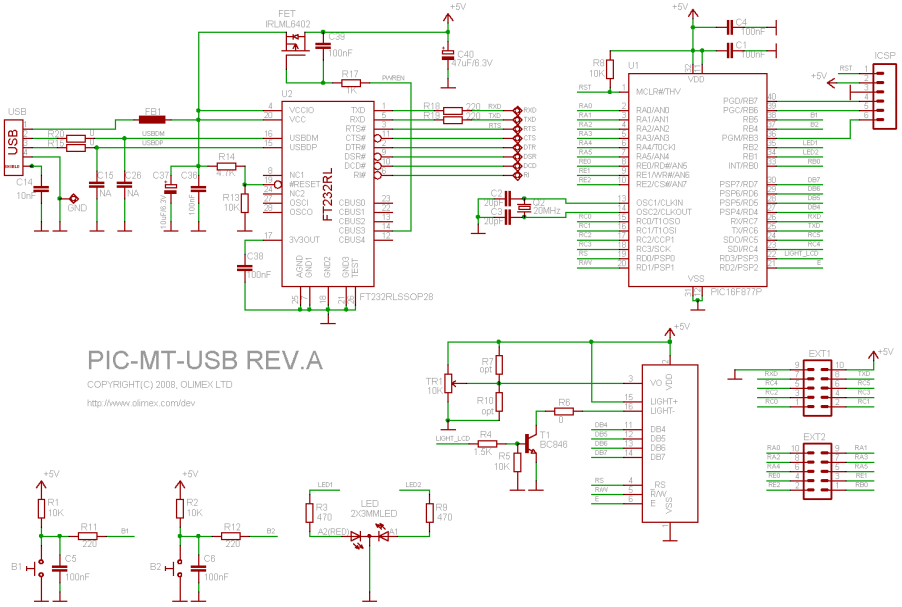


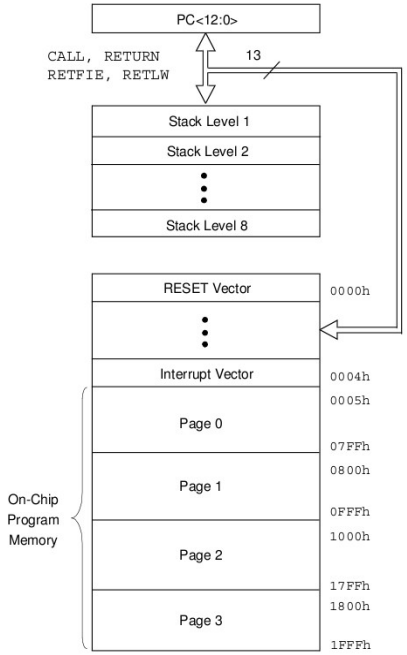
Conversion Analogique – numérique 10 bits



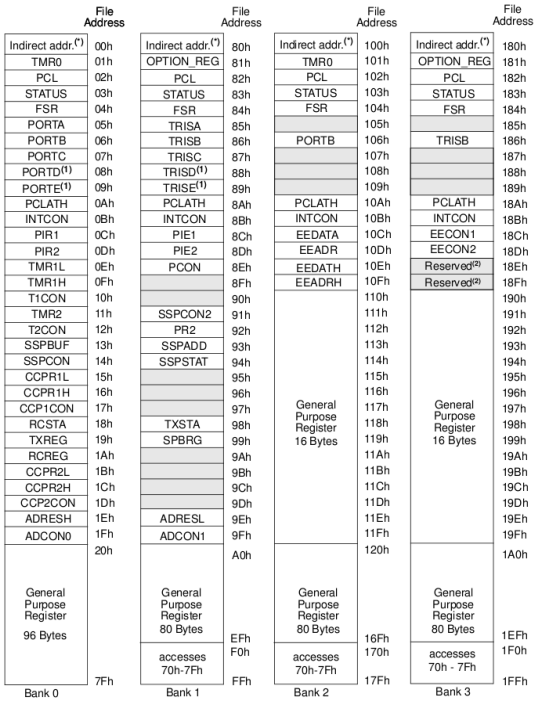
Electronique autour d'un microcontrôleur pour chenillard

PIC – MT - USB





La pile et le système de pages du PIC 16F877c



RP1:RP0	Bank
00	0
01	1
10	2
11	3

Les quatre banques de registres du PIC 16F877