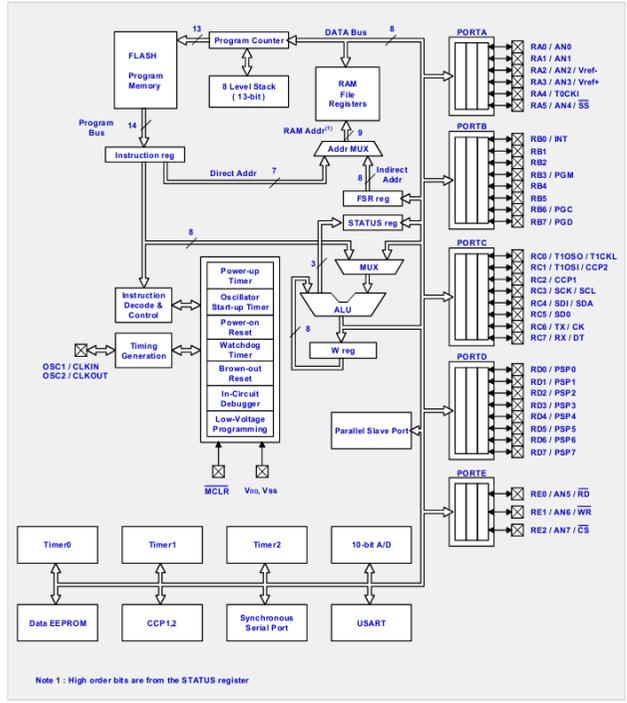
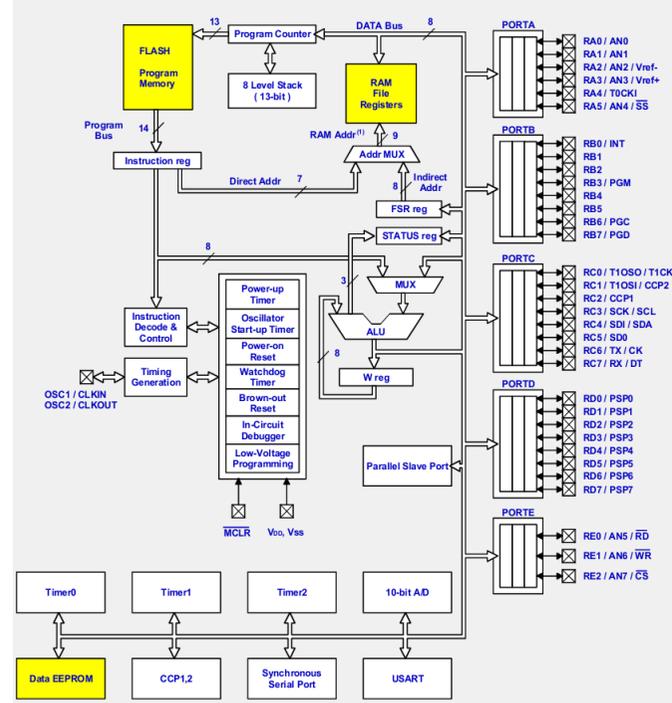


Le CPU du PIC 16F877

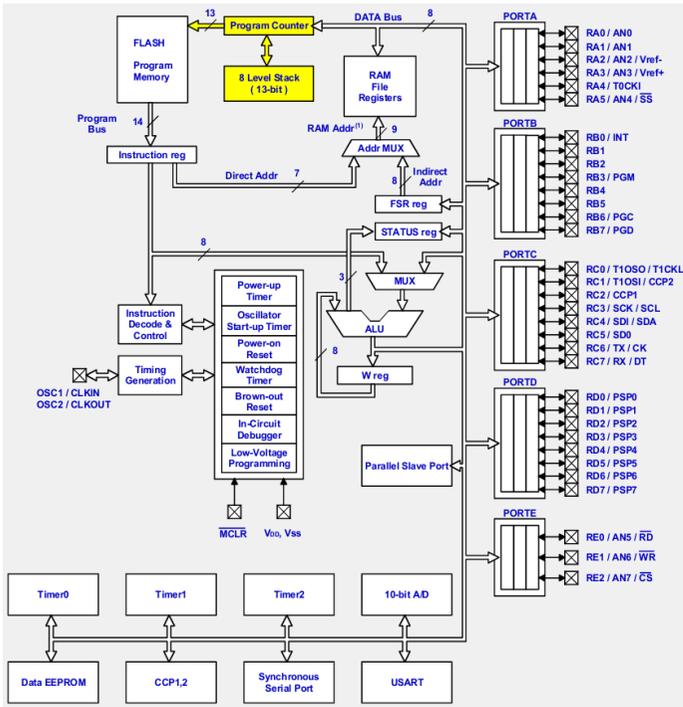


Structure interne du PIC 16F877

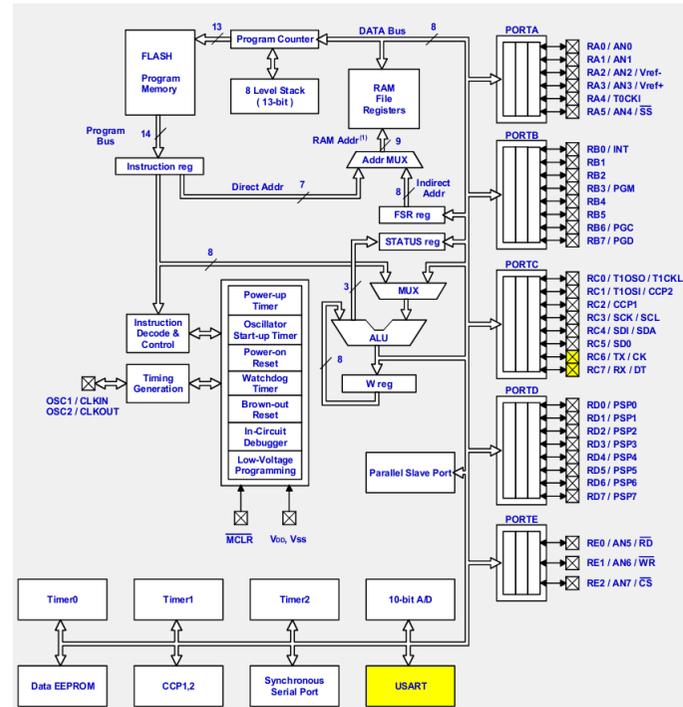


Les mémoires du PIC 16F877

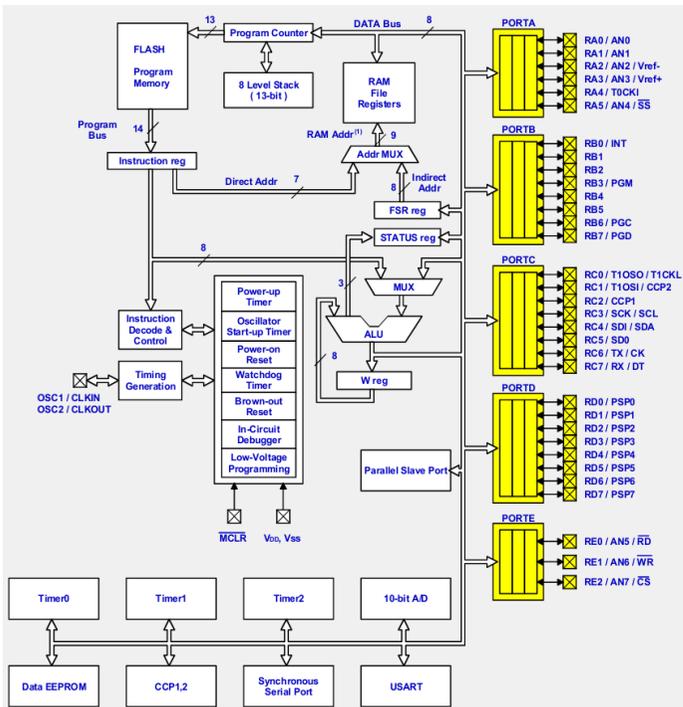
Note 1 : High order bits are from the STATUS register



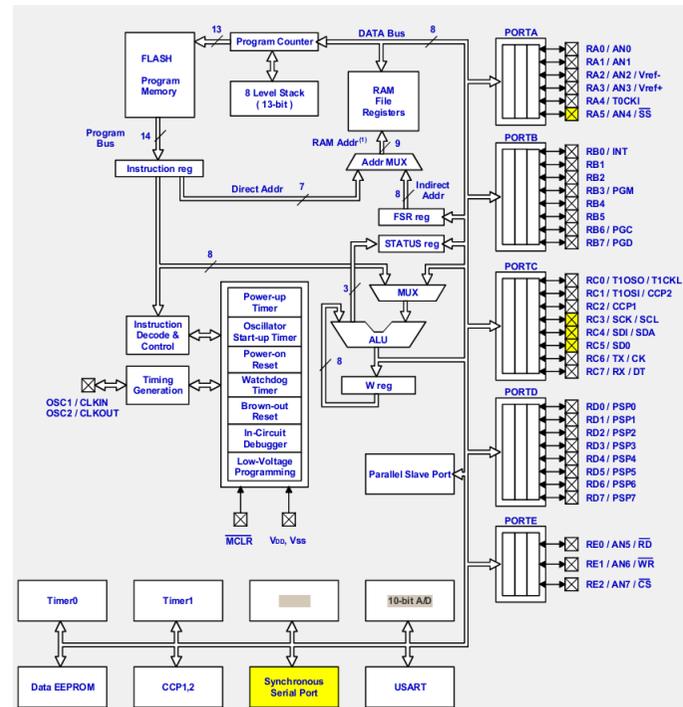
Le système de pile du PIC 16F877



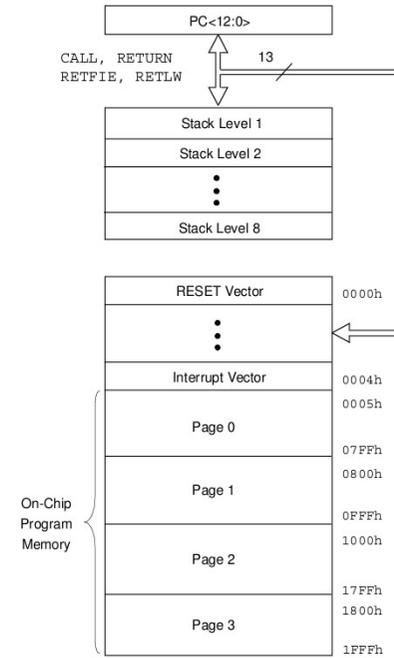
L'USART (Universal Asynchronous Synchronous Receiver Transmitter) du PIC 16F877



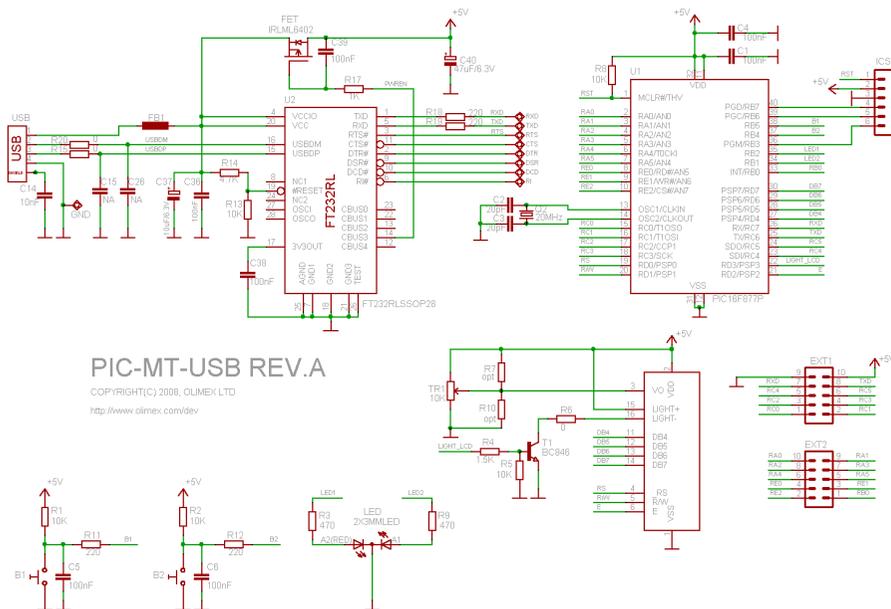
Les ports d'E/S du PIC 16F877



PIC – MT - USB



La pile et le système de pages du PIC 16F877c



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File Address	File Address	File Address	File Address
Indirect addr. ⁽¹⁾ 00h	Indirect addr. ⁽¹⁾ 80h	Indirect addr. ⁽¹⁾ 100h	Indirect addr. ⁽¹⁾ 180h
TMR0 01h	OPTION_REG 81h	TMR0 101h	OPTION_REG 181h
PCL 02h	PCL 82h	PCL 102h	PCL 182h
STATUS 03h	STATUS 83h	STATUS 103h	STATUS 183h
FSR 04h	FSR 84h	FSR 104h	FSR 184h
PORTA 05h	TRISA 85h	PORTB 105h	TRISA 185h
PORTB 06h	TRISB 86h	PORTC 106h	TRISB 186h
PORTC 07h	TRISC 87h	PORTD ⁽¹⁾ 107h	TRISC 187h
PORTD ⁽¹⁾ 08h	TRISD ⁽¹⁾ 88h	TRISE ⁽¹⁾ 108h	TRISD ⁽¹⁾ 188h
PORTE ⁽¹⁾ 09h	TRISE ⁽¹⁾ 89h	PCLATH 109h	TRISE ⁽¹⁾ 189h
PCLATH 0Ah	PCLATH 8Ah	PCLATH 10Ah	PCLATH 18Ah
INTCON 0Bh	INTCON 8Bh	INTCON 10Bh	INTCON 18Bh
PIR1 0Ch	PIE1 8Ch	EEDATA 10Ch	ECON1 18Ch
PIR2 0Dh	PIE2 8Dh	EEDATA 10Dh	ECON2 18Dh
TMR1H 0Eh	PCON 8Eh	EEDATH 10Eh	Reserved ⁽²⁾ 18Eh
TMR1L 0Fh	EEDATH 8Fh	EEDATH 10Fh	Reserved ⁽²⁾ 18Fh
TMR2 10h	EEDATH 90h	Reserved ⁽²⁾ 110h	Reserved ⁽²⁾ 190h
SSPBUF 13h	SSPCON2 91h	Reserved ⁽²⁾ 111h	Reserved ⁽²⁾ 191h
SSPCON 14h	PR2 92h	Reserved ⁽²⁾ 112h	Reserved ⁽²⁾ 192h
CCPR1L 15h	SSPADD 93h	Reserved ⁽²⁾ 113h	Reserved ⁽²⁾ 193h
CCPR1H 16h	SSPSTAT 94h	Reserved ⁽²⁾ 114h	Reserved ⁽²⁾ 194h
CCP1CON 17h	CCPR2L 95h	Reserved ⁽²⁾ 115h	Reserved ⁽²⁾ 195h
RCSTA 18h	CCPR2H 96h	Reserved ⁽²⁾ 116h	Reserved ⁽²⁾ 196h
TXREG 19h	CCP2CON 97h	General Purpose Register 16 Bytes 117h	General Purpose Register 16 Bytes 197h
RCREG 1Ah	RCSTA 98h	General Purpose Register 16 Bytes 118h	General Purpose Register 16 Bytes 198h
CCPR2L 1Bh	TXREG 99h	General Purpose Register 16 Bytes 119h	General Purpose Register 16 Bytes 199h
CCPR2H 1Ch	RCREG 9Ah	General Purpose Register 16 Bytes 120h	General Purpose Register 16 Bytes 200h
ADRESH 1Eh	CCPR2L 9Bh	General Purpose Register 16 Bytes 121h	General Purpose Register 16 Bytes 201h
ADCON0 1Fh	CCPR2H 9Ch	General Purpose Register 16 Bytes 122h	General Purpose Register 16 Bytes 202h
ADCON1 1Fh	ADRESL 9Dh	General Purpose Register 16 Bytes 123h	General Purpose Register 16 Bytes 203h
General Purpose Register 96 Bytes 7Fh	ADRESH 9Eh	General Purpose Register 16 Bytes 124h	General Purpose Register 16 Bytes 204h
	ADCON1 9Fh	General Purpose Register 16 Bytes 125h	General Purpose Register 16 Bytes 205h
	ADCON1 A0h	General Purpose Register 16 Bytes 126h	General Purpose Register 16 Bytes 206h
	General Purpose Register 80 Bytes EFh	General Purpose Register 16 Bytes 127h	General Purpose Register 16 Bytes 207h
	accesses 70h-7Fh F0h	General Purpose Register 80 Bytes 16Fh	General Purpose Register 16 Bytes 208h
		accesses 70h-7Fh 170h	General Purpose Register 80 Bytes 1EFh
		accesses 70h-7Fh 17Fh	General Purpose Register 80 Bytes 1FFh
Bank 0	Bank 1	Bank 2	Bank 3

RP1:RP0	Bank
00	0
01	1
10	2
11	3

Les quatre banques de registres du PIC 16F877