MICROCHIP

PIC 16F877 (pinout diagram)

Pin	Left		Pin	Right
1	$\overline{MCLR}$/Vpp		40	RB7/PGD
2	RA0/AN0		39	RB6/PGC
3	RA1/AN1		38	RB5
4	RA2/AN2/VRef-		37	RB4
5	RA3/AN3/VRef+		36	RB3/PGM
6	RA4/T0CKI		35	RB2
7	RA5/AN4/$\overline{SS}$		34	RB1
8	RE0/$\overline{RD}$/AN5		33	RB0/INT
9	RE1/$\overline{WR}$/AN6		32	VDD
10	RE2/$\overline{CS}$/AN7		31	VSS
11	VDD		30	RD7/PSP7
12	VSS		29	RD6/PSP6
13	OSC1/CLKIN		28	RD5/PSP5
14	OSC2/CLKOUT		27	RD4/PSP4
15	RC0/T1OSO/T1CKI		26	RC7/RX/DT
16	RC1/T1OSI/CCP2		25	RC6/TX/CK
17	RC2/CCP1		24	RC5/SDO
18	RC3/SCK/SCL		23	RC4/SDI/SDA
19	RD0/PSP0		22	RD3/PSP3
20	RD1/PSP1		21	RD2/PSP2

Le CPU du PIC 16F877

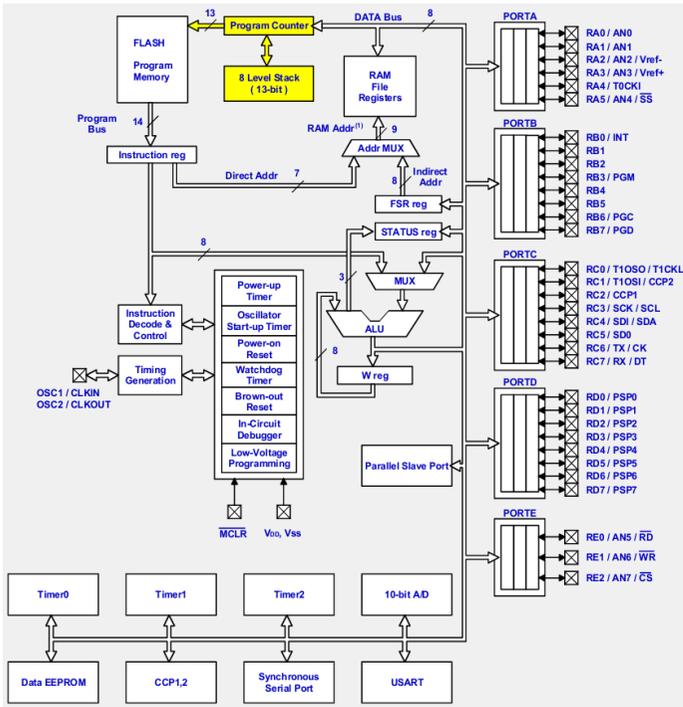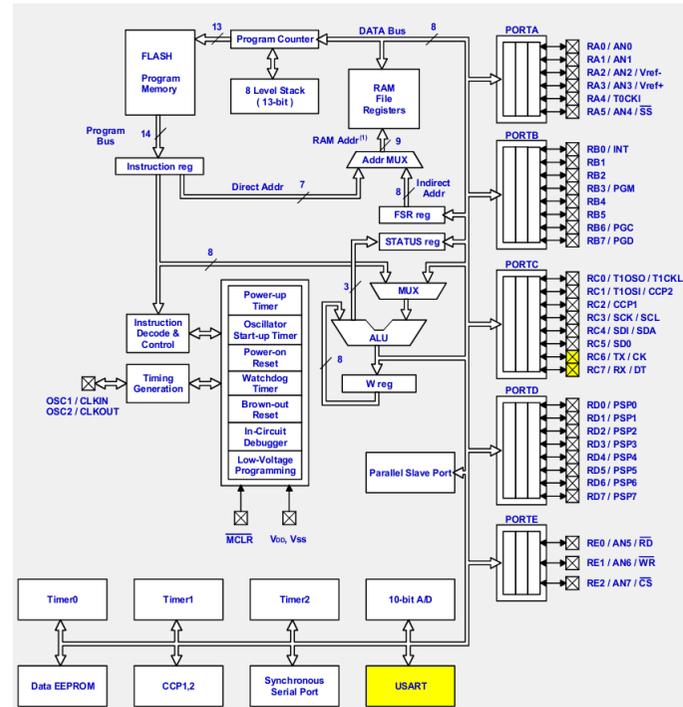**Structure interne du PIC 16F877**

Note 1 : High order bits are from the STATUS register
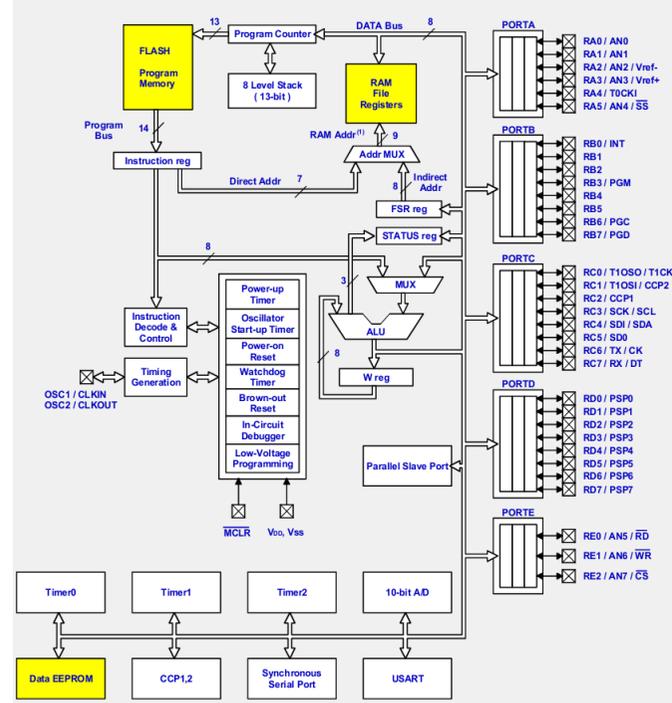
Les mémoires du PIC 16F877
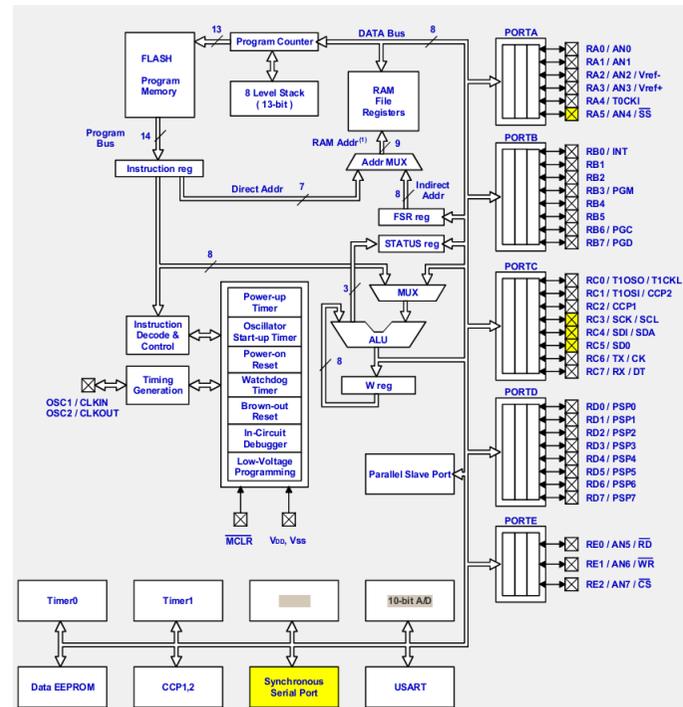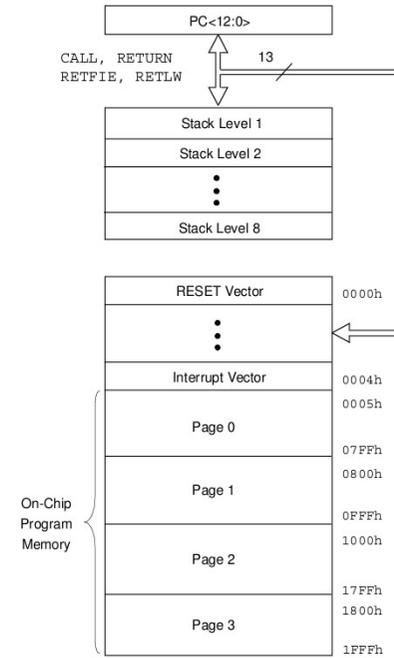
Le système de pile
du PIC 16F877

L'USART (Universal
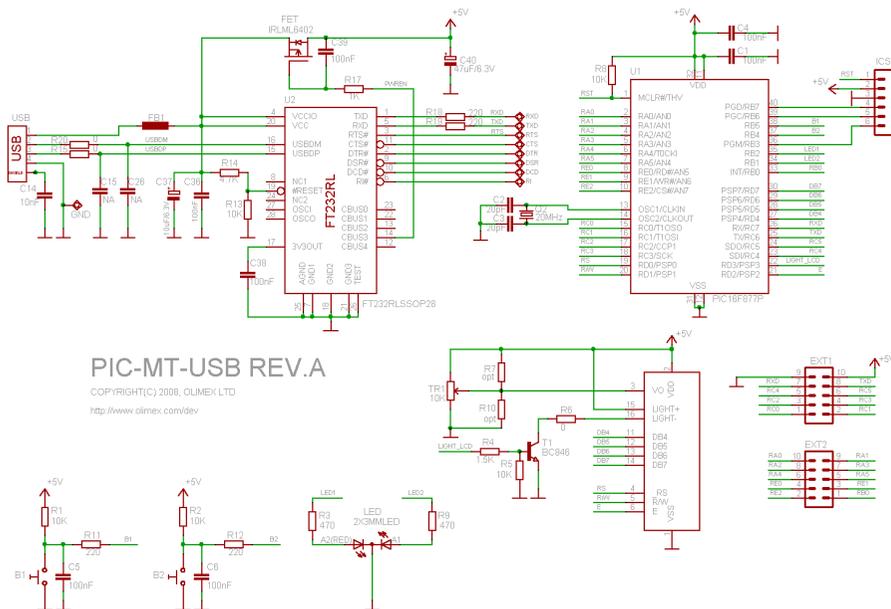Asynchronous
Synchronous
Receiver
Transmitter)
du PIC 16F877
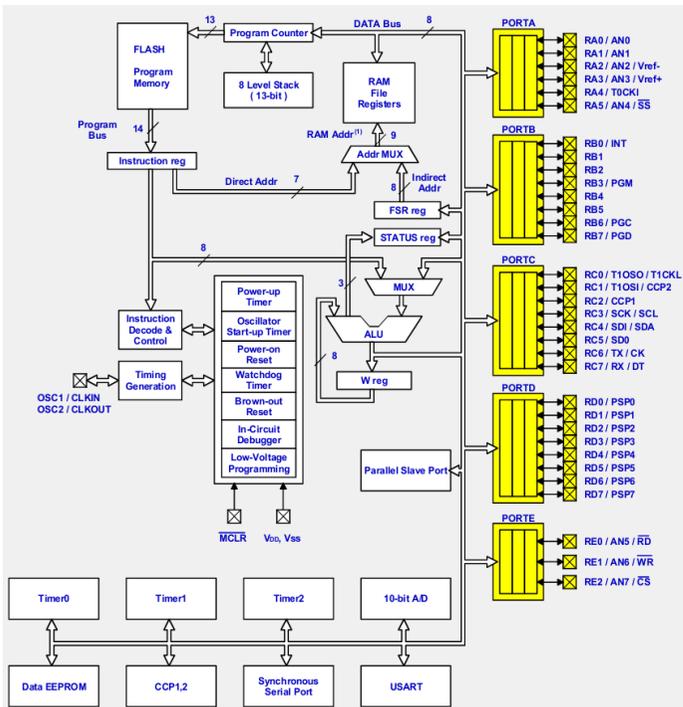
Les ports d'E/S
du PIC 16F877

Timer0 du PIC 16F877

Le système ICSP (In-Circuit Serial Programming) du PIC 16F877

Conversion Analogique – numérique 10 bits

Electronique autour d'un microcontrôleur pour chenillard

PIC – MT - USB

PC<12:0>

CALL, RETURN
RETFIE, RETLW   13

Stack Level 1
Stack Level 2
⋮
Stack Level 8

RESET Vector   0000h

⋮

Interrupt Vector   0004h

On-Chip Program Memory		
Page 0	0005h / 07FFh	
Page 1	0800h / 0FFFh	
Page 2	1000h / 17FFh	
Page 3	1800h / 1FFFh	

La pile et le système de pages du PIC 16F877c

RP1:RP0	Bank
00	0
01	1
10	2
11	3

Les quatre banques de registres du PIC 16F877

PIC-MT-USB REV.A
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