

Best Paper Awards

Each year the Design, Automation and Test in Europe Conference presents awards to the authors of the most outstanding papers of the previous year's conference. The selection is performed by an award committee, based on the results of the reviewing process, the quality of the final paper and the quality of the presentation.

The paper selected as the most outstanding in the field of CAD (track A) is:

Digital Ground Bounce Reduction by Phase Modulation of the Clock

*by Mustafa Badaroglu, Georges Gielen and Hugo De Man, IMEC and KU Leuven, Belgium
and Piet Wambacq, Geert Van der Plas and Stephane Donnay, IMEC, Belgium*

The paper selected as the most outstanding in the field of Tools (track B) is:

Poor Man's TBR: A Simple Model Reduction Scheme

*by Joel Phillips, Cadence Berkeley Laboratories, USA
and
Luis Miguel Silveira, INESC-ID/IST – TU Lisbon, Portugal*

The paper selected as the most outstanding in the field of Test (track C) is:

Wrapper Design for Testing IP Cores with Multiple Clock Domains

by Qiang Xu and Nicola Nicolici, McMaster University, Canada

The paper selected as the most outstanding Interactive Presentation (track IP) is:

Automatic Synthesis and Simulation of Continuous-Time Sigma-Delta Modulators

*by Hassan Aboushady, Laurent De Lamarre, Nicolas Beilleau and Marie-Minerve Louerat,
Paris VI University, France*

Congratulations to the winners!