

Feedback

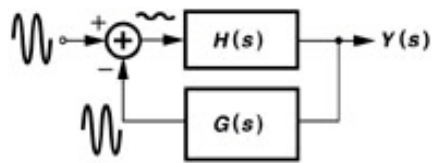
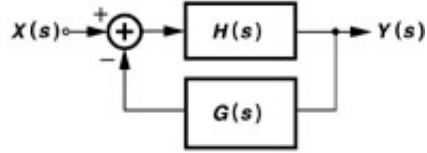
***Hassan Aboushady
University of Paris VI***

References

- **B. Razavi, "Design of Analog CMOS Integrated Circuits", McGraw-Hill, 2001.**

Feedback

$$\frac{Y(s)}{X(s)} = \frac{H(s)}{1 + G(s)H(s)}$$

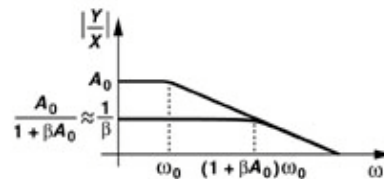
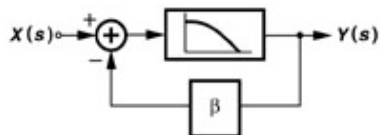


H. Aboushady

University of Paris VI

Feedback

$$\frac{Y(s)}{X(s)} = \frac{H(s)}{1 + G(s)H(s)}$$



$$\left. \begin{array}{l} H(s) = \frac{A_0}{1 + s/\omega_0} \\ G(s) = \beta \end{array} \right\} \frac{Y(s)}{X(s)} = \frac{\frac{A_0}{1 + s/\omega_0}}{1 + \frac{s}{(1 + \beta A_0)\omega_0}}$$

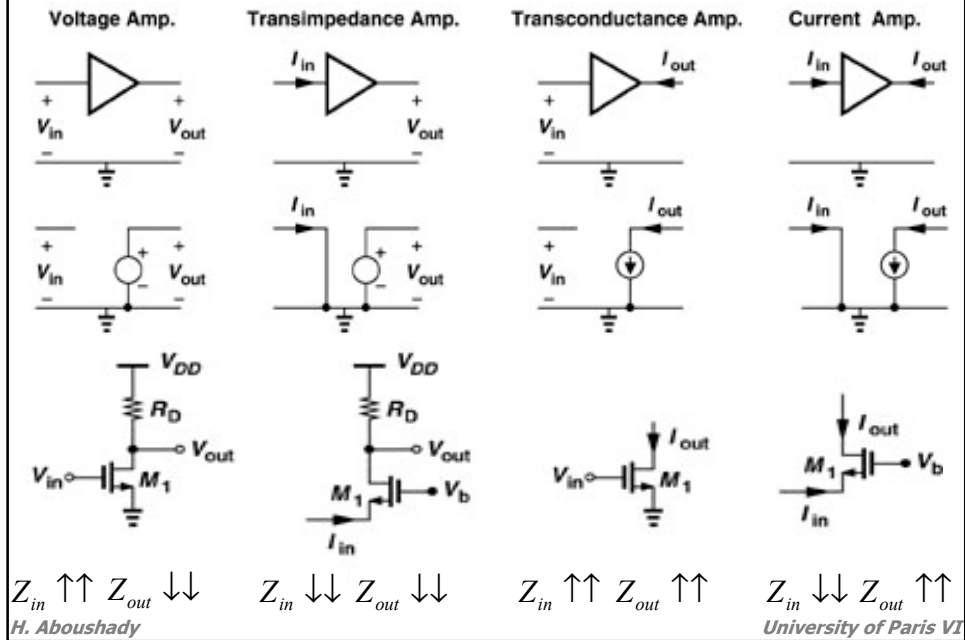
- Gain is decreased
- Bandwidth is increased by the same factor:

$$\Rightarrow \boxed{1 + \beta A_0}$$

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Types of Amplifiers



Voltage-Voltage Feedback (series-shunt)

$$V_F = \beta V_{out} \quad V_e = V_{in} - V_F$$

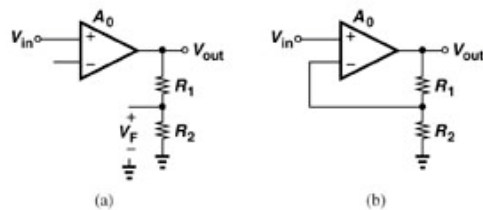
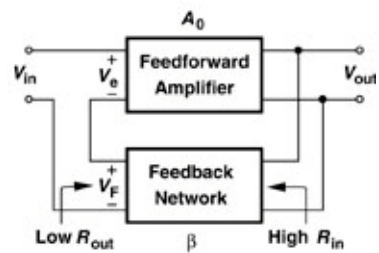
$$V_{out} = A_0 (V_{in} - \beta V_{out})$$

$$\boxed{\frac{V_{out}}{V_{in}} = \frac{A_0}{1 + \beta A_0}}$$

• **Example:**

$$\beta = \frac{V_F}{V_{out}} = \frac{R_2}{R_1 + R_2}$$

$$\Rightarrow \frac{V_{out}}{V_{in}} = \frac{A_0}{1 + \frac{A_0 R_2}{R_1 + R_2}}$$

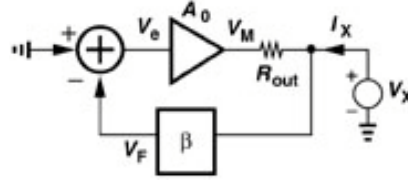


Output Resistance of Voltage-Voltage Feedback

$$V_F = \beta V_X$$

$$V_e = -\beta V_X$$

$$V_M = -\beta A_0 V_X$$



Neglecting the current in the feedback path:

$$I_X = \frac{V_X - V_M}{R_{out}}$$



$$Z_{out} = \frac{V_X}{I_X} = \frac{R_{out}}{1 + \beta A_0}$$

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Example

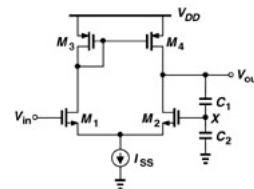
Open loop Gain:

$$A_0 = g_{m1}(r_{O2} // r_{O4})$$

Depends
on $r_{O1} // r_{O2}$

Feedback Gain:

$$\beta = \frac{V_X}{V_{out}} = \frac{C_1}{C_1 + C_2}$$



Closed loop Gain:

$$\frac{V_{out}}{V_{in}} = \frac{A_0}{1 + \beta A_0} = \frac{g_{m1}(r_{O1} // r_{O2})}{1 + \frac{C_1}{C_1 + C_2} g_{m1}(r_{O1} // r_{O2})} \approx 1 + \frac{C_2}{C_1}$$

Independent
of $r_{O1} // r_{O2}$

Closed loop Output Resistance:

$$\frac{V_X}{I_X} = \frac{R_{out}}{1 + \beta A_0} = \frac{r_{O2} // r_{O4}}{1 + \frac{C_1}{C_1 + C_2} g_{m1}(r_{O2} // r_{O4})} \approx \left(1 + \frac{C_2}{C_1}\right) \frac{1}{g_{m1}}$$

Independent
of $r_{O1} // r_{O2}$

H. Aboushady

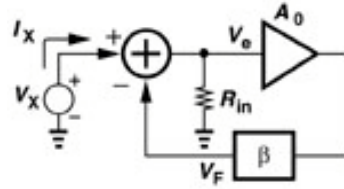
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Input Resistance of Voltage-Voltage Feedback

$$V_F = \beta A_0 I_X R_{in}$$

$$V_e = I_X R_{in} = V_X - V_F$$

$$I_X R_{in} = V_X - \beta A_0 I_X R_{in}$$



Neglecting the current in the feedback path:

$$\Rightarrow Z_{in} = \frac{V_X}{I_X} = R_{in} (1 + \beta A_0)$$

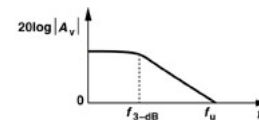
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Operational Amplifier: Performance Parameters

Gain: the open loop gain of an op-amp determines the precision of the feedback system employing the op-amp

Small Signal Bandwidth:
Unity-Gain freq., f_u , and the 3dB freq., f_{3-dB} .



Large Signal Bandwidth (slew rate):
Op-Amp response to large transient signals.

Output Swing:

Linearity: non-linearity can be reduced by using a differential circuit and by increasing the open-loop gain in a feedback system

Noise and Offset: input noise and offset determine the minimum signal level that can be processed with reasonable quality.

Supply Rejection:

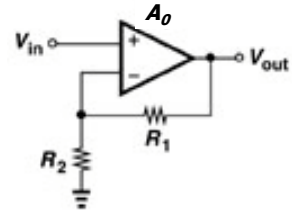
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Op-Amp Gain

Example: This circuit is designed for a gain of 10.
Determine the minimum value of A_0 for a gain error of 1%.

$$\frac{V_{out}}{V_{in}} = \left(1 + \frac{R_1}{R_2}\right) \frac{1}{1 + \frac{1 + R_1/R_2}{A_0}}$$



Ideally, $A_0 \rightarrow \infty \Rightarrow \text{gain} = 1 + R_1 / R_2$

Taking, $A_0 \gg 1 + R_1 / R_2$, we have

$$\frac{V_{out}}{V_{in}} \approx \left(1 + \frac{R_1}{R_2}\right) \left(1 - \frac{1 + R_1 / R_2}{A_0}\right) \quad \Rightarrow \quad \text{gain error} = \frac{1 + R_1 / R_2}{A_0}$$

Compare with the gain error of a CS stage!